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PN - JP63058553 A 19880314
PD - 1988-03-14
PR - JP19860201532 19860829
OPD - 1986-08-29
TI - DISPLAY SYSTEM FOR ERROR COUNTERMEASURE OF ELECTRONIC COMPUTER SYSTEM
IN - TANAKA SHIRO
PA - TOKYO SHIBAURA ELECTRIC CO
IC - G06F11/32

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TI - DISPLAY SYSTEM FOR ERROR COUNTERMEASURE OF ELECTRONIC COMPUTER SYSTEM
AB - PURPOSE: To perform the quick processing with a simple operation by providing a message file into a memory device to store the identification code of an error, the corresponding message number and a message text respectively.
- CONSTITUTION: With input of an error identification code produced from a display device 1, a code processing means 3 extracts a factor code corresponding to said identification code out of a code combination table 6 of a memory device 5. Then the corresponding factor message sentence is taken out of a factor message table 7. At this time, a countermeasure code for recovering error is also taken out. Then a countermeasure message sentence is taken out of a countermeasure message table 8. These extracted factor and countermeasure message sentence are displayed on the device 1 or a printer 2 via a message output means 4. With input of plural error identification codes, an error factor common with each error is detected through the table 6 and therefore an optimum factor countermeasure message sentence is selected and delivered.
I - G06F11/32

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特開昭63-58553 (3)

においても同様な効果が得られる。

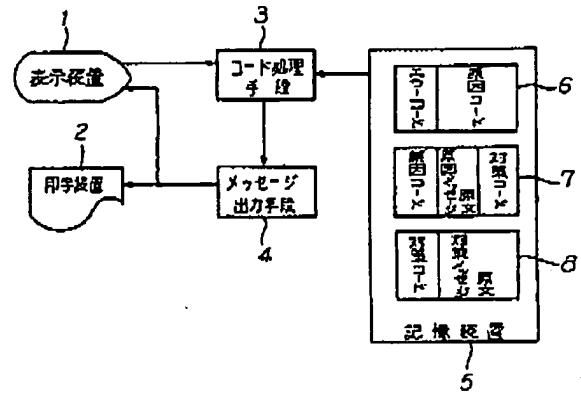
〔発明の効果〕

以上説明した如く、本発明によれば、発生したエラーの識別コードを入力するだけの簡単な操作で、エラーに対する原因と対策を出力表示することが可能である。

4、図面の簡単な説明

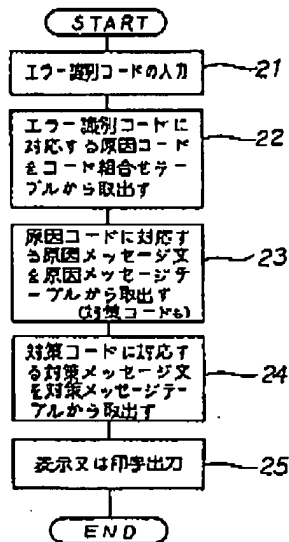
第1図は本発明の一実施例を示す構成図、第2図は動作説明のフローチャートである。

- 1…表示装置 2…印字装置
3…コード処理手段 4…メッセージ出力手段
5…記憶装置
6…コード組合せテーブル
7…原因メッセージテーブル
8…対策メッセージテーブル



第1図

代理人 井理士 剛 述 彦 伯
岡 三 俣 弘 文



第2図

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